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8-2-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kiochiro TANAKA  
Serial No. : 09/842,797  
Filed : April 27, 2001  
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Art Unit : 2822  
Examiner : Maria Guerrero

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY**

In reply to the action mailed February 12, 2003, please amend the application as indicated on the following pages. Applicant asks that all claims be allowed in view of the amendment to the claims and remarks contained on the following sheets, a total of 16 pages (includes four pages of amended drawings).

Enclosed is a \$410 check for the Petition for Extension of Time fee. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 14, 2003

  
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John F. Hayden  
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